

FILE ID**MPCLRPFM

D 14

MP
VO

A 4x4 grid of black dots arranged in four rows and four columns.

The diagram illustrates a sequence of binary strings. On the left, there is a vertical column of strings starting with 'L' at the top, followed by 'LL', 'LLL', 'LLLL', 'LLLLL', 'LLLLLL', 'LLLLLLL', 'LLLLLLLL', 'LLLLLLLLL', and ending with 'LLLLLLLLL' at the bottom. To the right of a vertical bar, there is another vertical column of strings starting with 'S' at the top, followed by 'SS', 'SSS', 'SSSS', 'SSSSS', 'SSSSSS', and ending with 'SSSSSSS' at the bottom. The strings are aligned such that each 'L' in the first column corresponds to an 'S' in the second column, creating a visual representation of a mapping or transformation between two sets of binary sequences.

```
0000 1 .TITLE MPCLRPFM
0000 2 .IDENT 'V04-000'
0000 3
0000 4
0000 5 ****
0000 6 :*
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0000 24 :*
0000 25 :*
0000 26 ****
0000 27 :
0000 28 :
0000 29 :++
0000 30 :
0000 31 : Facility: Multi-processor performance measurement tool to re-init data cells
0000 32 :
0000 33 : Abstract: This module resets all performance accumulators to zero.
0000 34 :
0000 35 : Environment: MODE=Kernel
0000 36 :
0000 37 : Author: Kathleen D. Morse, Creation date: 27-Aug-1981
0000 38 :
0000 39 : Modified by:
0000 40 :
0000 41 : V03-002 KDM0032 Kathleen D. Morse 22-Nov-1982
0000 42 : Remove wait time before secondary reschedule histogram.
0000 43 : Add secondary executed kernel system service histogram.
0000 44 :
0000 45 :
0000 46 :--
0000 47 :
0000 48 :
0000 49 : Include files:
0000 50 :
0000 51 :
0000 52 :
0000 53 : MACROS:
0000 54 :
0000 55 :
0000 56 :
0000 57 : Equated Symbols:
```

```

0000 59 .
0000 60 $IPLDEF
0000 61 $PCBDEF
0000 62 $PHDDEF
0000 63
0000 64
00000000 0000 65 HST_L_CELLCOUNT = 0
00000004 0000 66 HST_L_CELLWIDTH = 4
00000008 0000 67 HST_L_OVRFLW = 8
00000010 0000 68 HST_L_FIRSTCELL = 16
00000000 0000 69
00000000 0000 70 .PSECT RO_DATA LONG,NOWRT,NOEXE
00000000 0000 71
00000000 0000 72 .PSECT CODE BYTE,NOWRT,EXE
00000000 0000 73 CLRPFM:: .PSECT CODE BYTE,NOWRT,EXE
00000000 0000 74 CLRPFM:: .ENABL LSB
00000000 0000 75 .WORD 0
0002 76 $CMKRNLS B^CLRDATA
000E 77 $EXIT_S-RO
0017 78
0017 79 : CLRDATA - This routine goes into kernel mode and clears the performance
0017 80 : data.
0017 81 :
0017 82 :
0017 83 CLRDATA:: .WORD ^M<R2,R3,R4,R5,R6>
007C 0017 84 SETIPL #IPL$_TIMER ;Synchronize on primary processor
50 00000000'GF 50 D4 001C 85 CLRL R0 ;Assume error code exit
03 00000000'GF 00 D0 001E 86 MOVL G^EXESGL_MP,R6 ;Get adr of loaded MP code
00F4 00000000'GF 03 12 0025 87 BNEQ 5$ ;Br if MP code is loaded
00000000'GF 00F4 31 0027 88 BRW ERR_EXIT ;Br if MP code not loaded
002A 90
002A 91 5$: MOVZBL #6,R1 ;One counter for K,E,S,U,I,C and null
0000'C641 0000'C641 9A 002A 92 10$: CLRL MPSSAL_CPUTIME(R6)[R1]
F8 51 F4 002D 93 SOBGEQ R1,10$ ;R1,10$-
0032 94
0035 95
0035 96 20$: MOVZBL #5,R1
00000000'GF41 00000000'GF41 9A 0038 97 CLRL G^PMSSGL_KERNEL[R1]
F6 51 F4 003F 98 SOBGEQ R1,20$ ;R1,20$-
0042 98
0042 99
0042 100 MOVAB G^SCH$GL_NULLPCB,R0
50 00000000'GF 50 6C A0 D0 0049 101 MOVL PCB$L_PHD(R0),R0
38 A0 D4 004D 102 CLRL PHD$L_CPUTIM(R0)
0050 102
0000'C6 D4 0050 103 CLRL PFMSL_CNT_CTXSW(R6)
0000'C6 D4 0054 104 CLRL PFMSL_CNT_RESCH(R6)
0000'C6 D4 0058 105 CLRL PFMSL_CNT_SCHDS(R6)
0000'C6 D4 005C 106 CLRL PFMSL_CNT_INVAL(R6)
0000'C6 D4 0060 107 CLRL PFMSL_CNT_IWAIT(R6)
0000'C6 D4 0064 108 CLRL PFMSL_CNT_EXCHG(R6)
0000'C6 D4 0068 109 CLRL PFMSL_CNT_ASTSC(R6)
0000'C6 D4 006C 110 CLRL PFMSL_CNT_NWAIT(R6)
0070 111
0070 112 MOVAB PFMSA_HIST_TIME(R6),R0
50 0000'C6 9E 0070 113 MULL3 HST_L_CELLCOUNT(R0),#4,R1
51 04 60 C5 0075 114 ADDL #12,RT ;Add in overflow cell
      0C CO 0079 114

```

60 51 00	50 08	C0 007C	115	ADDL	#HST_L_OVRFLOW,R0	
	60 00	2C 007F	116	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 0085	117	MOVAB	PFMSA_HIST_SRV(R6),R0	
51 04	60 C5	008A	118	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 008E	119	ADDL	#12_RT		
50 08	CO 0091	120	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cell	
60 51 00	60 00	2C 0094	121	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 009A	122	MOVAB	PFMSA_HIST_CTX(R6),R0	
51 04	60 C5	009F	123	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 00A3	124	ADDL	#12_RT		
50 08	CO 00A6	125	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cell	
60 51 00	60 00	2C 00A9	126	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 00AF	127	MOVAB	PFMSA_HIST_PGFL(R6),R0	
51 04	60 C5	00B4	128	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 00B8	129	ADDL	#12_RT		
50 08	CO 00BB	130	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cell	
60 51 00	60 00	2C 00BE	131	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 00C4	132	MOVAB	PFMSA_HIST_CHMK(R6),R0	
51 04	60 C5	00C9	133	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 00CD	134	ADDL	#12_RT		
50 08	CO 00D0	135	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cell	
60 51 00	60 00	2C 00D3	136	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 00D9	137	MOVAB	PFMSA_HIST_OTHR(R6),R0	
51 04	60 C5	00DE	138	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 00E2	139	ADDL	#12_RT		
50 08	CO 00E5	140	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cell	
60 51 00	60 00	2C 00E8	141	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 00EE	142	MOVAB	PFMSA_HIST_SSrv(R6),R0	
51 04	60 C5	00F3	143	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 00F7	144	ADDL	#12_RT		
50 08	CO 00FA	145	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cells	
60 51 00	60 00	2C 00FD	146	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 0103	147	MOVAB	PFMSA_HIST_KSRV(R6),R0	
51 04	60 C5	0108	148	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 010C	149	ADDL	#12_RT		
50 08	CO 010F	150	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cells	
60 51 00	60 00	2C 0112	151	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 0000'C6	9E 0118	152	MOVAB	PFMSA_HIST_KSRV(R6),R0	
51 04	60 C5	0118	153	MULL3	HST_L_CELLCOUNT(R0),#4,R1	
51 0C	CO 0118	154	ADDL	#12_RT		
50 08	CO 0118	155	ADDL	#HST_L_OVRFLOW,R0	;Add in overflow cells	
60 51 00	60 00	2C 0118	156	MOVCS	#0,(ROT,#0,R1,(R0)	;Clear performance meas data
	50 01	9A 011B	157	SETIPL	#0	;Reset IPL
		011E	158	MOVZBL	#1,R0	;Set success status
	04	011E	159	ERR_EXIT:	RET	
		011F	160			
		011F	161			
		011F	162			
		011F	163			
		011F	164			
		011F	165			
		011F	166			
		011F	167	.END	CLRPFM	

MPCLRPFM
Symbol table

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16-SEP-1984 02:13:37 VAX/VMS Macro V04-00
5-SEP-1984 02:06:07 [MP.SRC]MPCLRPFM.MAR;1

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CLRDAT	00000017	RG	03
CLRPFM	00000000	RG	03
ERR EXIT	0000011E	R	03
EXESGL MP	*****	X	03
HST_L_CELLCOUNT	= 00000000		
HST_L_CELLWIDTH	= 00000004		
HST_L_FIRSTCELL	= 00000010		
HST_L_OVRFLOW	= 00000008		
IPL\$ TIMER	= 00000008		
MPSSAL_CPUTIME	*****	X	03
PCBSL_PHD	= 0000006C		
PFMSA_HIST_CHMK	*****	X	03
PFMSA_HIST_CTX	*****	X	03
PFMSA_HIST_KSRV	*****	X	03
PFMSA_HIST_OTHR	*****	X	03
PFMSA_HIST_PGFL	*****	X	03
PFMSA_HIST_SRV	*****	X	03
PFMSA_HIST_SSRV	*****	X	03
PFMSA_HIST_TIME	*****	X	03
PFMSL_CNT_ASTSC	*****	X	03
PFMSL_CNT_CTXSW	*****	X	03
PFMSL_CNT_EXCHG	*****	X	03
PFMSL_CNT_INVAL	*****	X	03
PFMSL_CNT_IWAIT	*****	X	03
PFMSL_CNT_NWAIT	*****	X	03
PFMSL_CNT_RESCH	*****	X	03
PFMSL_CNT_SCHDS	*****	X	03
PHDSL_CPUTIM	= 00000038		
PMSSGE_KERNEL	*****	X	03
PR\$ IPC	*****	X	03
SCHSGL_NULLPCB	*****	X	03
SYSSCMRRNL	*****	GX	03
SYSEXIT	*****	GX	03

+-----+
! Psect synopsis !
+-----+

PSECT name

PSECT name	Allocation	PSECT No.	Attributes
ABS .	00000000	(0.) 00 (0.)	NOPIC USR CON ABS LCL NOSHR NOEXE NORD NOWRT NOVEC BYTE
\$ABSS	00000000	(0.) 01 (1.)	NOPIC USR CON ABS LCL NOSHR EXE RD WRT NOVEC BYTE
RO DATA	00000000	(0.) 02 (2.)	NOPIC USR CON REL LCL NOSHR NOEXE RD NOWRT NOVEC LONG
CODE	0000011F	(287.) 03 (3.)	NOPIC USR CON REL LCL NOSHR EXE RD NOWRT NOVEC BYTE

+-----+
! Performance indicators !
+-----+

Phase

Phase	Page faults	CPU Time	Elapsed Time
Initialization	29	00:00:00.09	00:00:00.80
Command processing	108	00:00:00.69	00:00:04.97
Pass 1	178	00:00:03.30	00:00:11.00
Symbol table sort	0	00:00:00.38	00:00:00.62
Pass 2	47	00:00:00.75	00:00:02.16
Symbol table output	5	00:00:00.05	00:00:00.09

MPCLRPFM
VAX-11 Macro Run Statistics

Psect synopsis output	2	00:00:00.03	00:00:00.04
Cross-reference output	0	00:00:00.00	00:00:00.00
Assembler run totals	372	00:00:05.30	00:00:19.68

The working set limit was 1050 pages.

16658 bytes (33 pages) of virtual memory were used to buffer the intermediate code.
 There were 20 pages of symbol table space allocated to hold 293 non-local and 3 local symbols.
 167 source lines were read in Pass 1, producing 16 object records in Pass 2.
 14 pages of virtual memory were used to define 13 macros.

```
+-----+
! Macro library statistics !
+-----+
```

Macro library name	Macros defined
\$255\$DUA28:[MP.OBJ]MP.MLB;1	0
\$255\$DUA28:[SYS.OBJ]LIB.MLB;1	4
\$255\$DUA28:[SYSLIB]STARLET.MLB;2	6
TOTALS (all libraries)	10

355 GETS were required to define 10 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LI\$:MPCLRPFM/OBJ=OBJ\$:MPCLRPFM MSRC\$:MPCLRPFM/UPDATE=(ENH\$:MPCLRPFM)+EXECMLS/LIB+LIBS:MP.MLB/LIB

16-SEP-1984 02:13:37 VAX/VMS Macro V04-00
 5-SEP-1984 02:06:07 [MP.SRC]MPCLRPFM.MAR;1

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0247 AH-BT13A-SE
VAX/VMS V4.0

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